

PATENT NUMBER and  
ISSUE DATE

U.S. UTILITY Patent Application

APPL NUM 10081546	FILING DATE 02/21/2002	CLASS 365	SUBCLASS 233	GAU 2818	EXAMINER MAI, SON
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\*\*APPLICANTS: Lee Jung-bae, La One-gyun,

\*\*CONTINUING DATA VERIFIED:

NONE - file

\*\* FOREIGN APPLICATIONS VERIFIED:

REPUBLIC OF KOREA 2001-24044 03/05/2001

REPUBLIC OF KOREA 2001-44065 07/21/2001

PG-PUB DO NOT PUBLISH ☐

RESCIND ☐

Foreign priority claimed

☒ yes ☐ no

35 USC 119 conditions met

☒ yes ☐ no

Verified and Acknowledged Examiners's Initials

AW

ATTORNEY DOCKET NO

8729-214 (SS-15661-US)

TITLE : Data input circuit and method for synchronous semiconductor memory device

U.S. DEPT. OF COMM./PAT. & TM./PTO-4350/Rev. 12-94

NOTICE OF ALLOWANCE MAILED		CLAIMS ALLOWED	
		Assistant Examiner	Application Examiner
ISSUE FEE		DRAWING	
Amount Due	Date Paid	Sheets Drawn	Fig. Drawn
		Print Claim for	Print Fig.
TERMINAL DISCLAIMER		Application Examiner	
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